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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Robert Reiner et al.  
Appl. No. : 09/771,886  
Filed : January 29, 2001  
Title : Clocked Integrated Semiconductor Circuit and Method for  
Operating Such a Circuit  
Art Unit :

ASSOCIATE POWER OF ATTORNEY

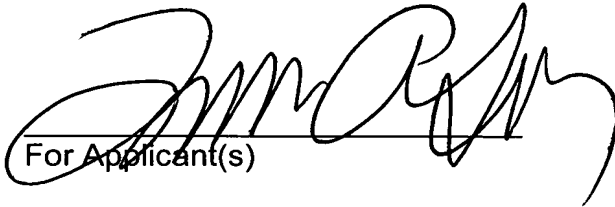
Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

Please recognize GREGORY L. MAYBACK (Reg. No. 40,719) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,

  
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